

# Open Hardware: Arduino and RISC-V

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- [OpenGradeSIM is an open source incline simulator for indoor bike trainers](#) [3]

Although he would probably rather be outdoors, after an injury Matt Ockendon had a lot more time to ride his Tacx Neo indoor trainer and tinker. He decided he wanted his rig to be able to simulate the grade of hills, but as commercially available units with this capability are quite expensive, he instead devised his own solution dubbed "OpenGradeSIM."

[...]

With this data in hand, the Nano controls a linear actuator using an L298N-based driver board to raise or lower the bike's front end. The derived bike angle is sensed via the Nano's built-in IMU, providing an elegant closed-loop system. Additionally, the incline is shown on a 1.3" I2C OLED display that serves as a mini dashboard while Ockendon cycles.

- [\[Reposted\] U.S.-Based Chip-Tech Group Moving to Switzerland Over Trade Curb Fears](#) [4]

- [RISC-V moves to neutral Switzerland over trade disruption](#) [5]

The nonprofit RISC-V Foundation is relocating to Switzerland over concerns about the impact of U.S. trade policies.

Foundation CEO Calista Redmond told Reuters that its members are "concerned about

possible geopolitical disruption.? The foundation?s board approved the move unanimously after hearing members around the globe say that they would be ?a lot more comfortable? if the incorporation were not in the U.S.,? she added.

RISC-V is a young organization, founded in 2015 and located in Delaware, to set standards for chip architectures that are allowed to use the RISC-V trademark on its products. RISC-V is a reduced instruction set architecture that is open source technology that anyone can use to design, make or sell RISC-V chips and software for electronics.

## [Hardware OSS](#)

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### **Links:**

[1] <http://www.tuxmachines.org/taxonomy/term/39>

[2] <http://www.tuxmachines.org/taxonomy/term/72>

[3] <https://www.blogdot.tv/opengradesim-is-an-open-source-incline-simulator-for-indoor-bike-trainers/>

[4] <https://www.nytimes.com/reuters/2019/11/25/business/25reuters-usa-china-semiconductors-insight.html>

[5] <https://www.fierceelectronics.com/electronics/risc-v-moves-to-neutral-switzerland-over-trade-disruption>