

# MIPS For Linux 3.16 Gets Big Changes

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Created *10/06/2014 - 10:47pm*

Submitted by Rianne Schestowitz on Tuesday 10th of June 2014 10:47:58 PM Filed under [Linux](#) [1]

The MIPS architecture pull for the Linux 3.16 merge window pull is full of prominent changes for this next kernel version.

First up, with the MIPS changes comes initial support for the Octeon 3. The Octeon 3 is Cavium's new multi-core processor line-up announced at the end of 2013. The OCTEON III is MIPS64-based and optimized for Wind River Linux and VxWorks. The Octeon III claims up to 120GHz of 64-bit processing and is aimed for high-performance computing environments.

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